

FIG.1

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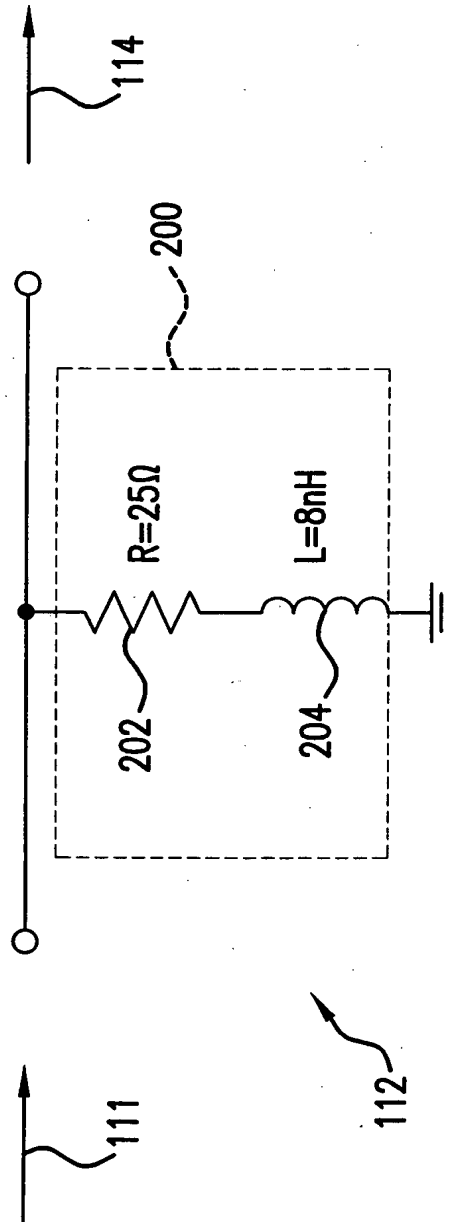


FIG. 2

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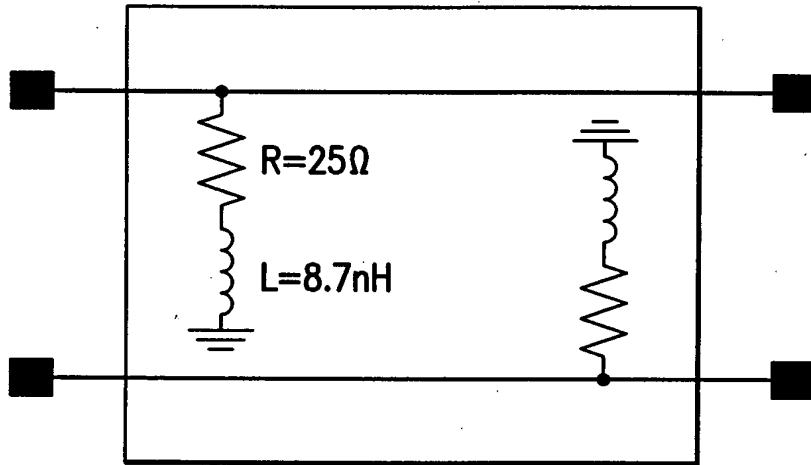


FIG.3A

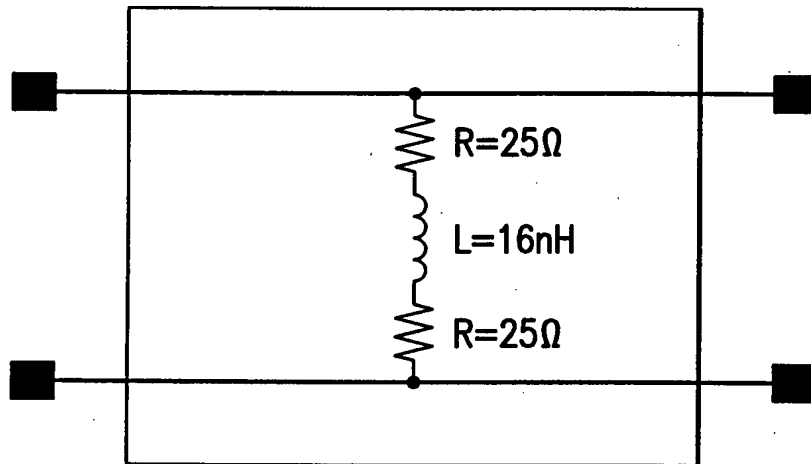


FIG.3B

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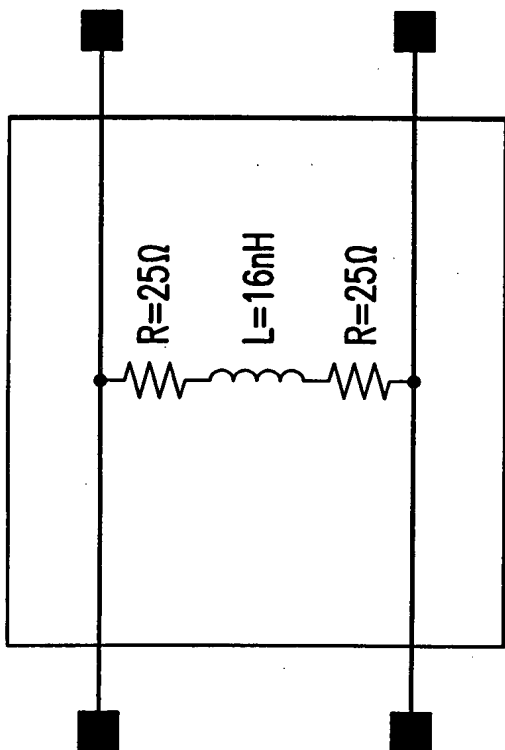


FIG. 3C

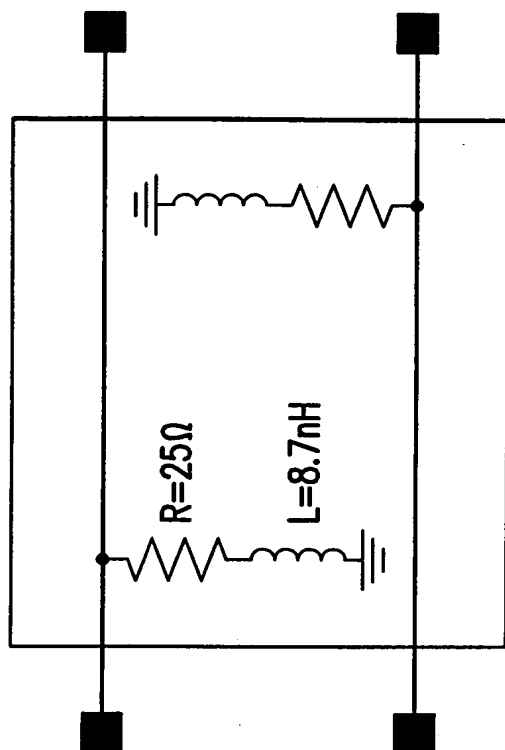


FIG. 3D

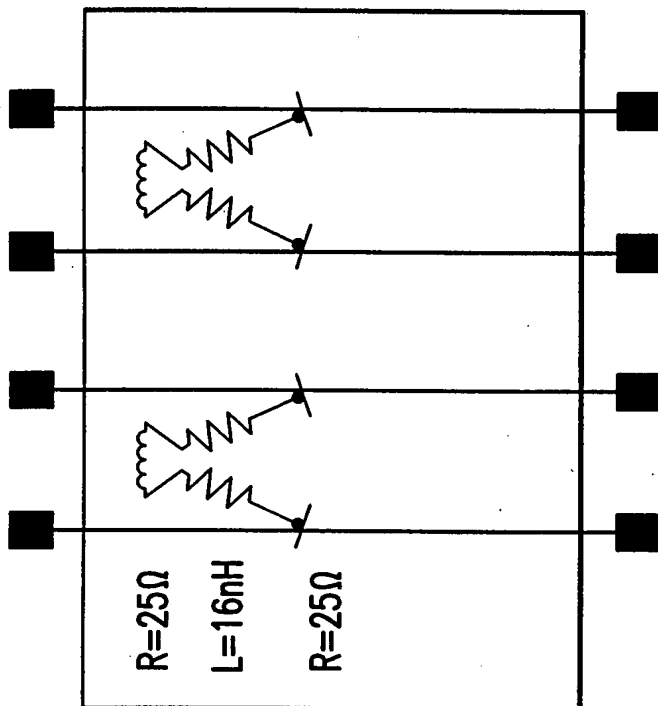


FIG. 3E

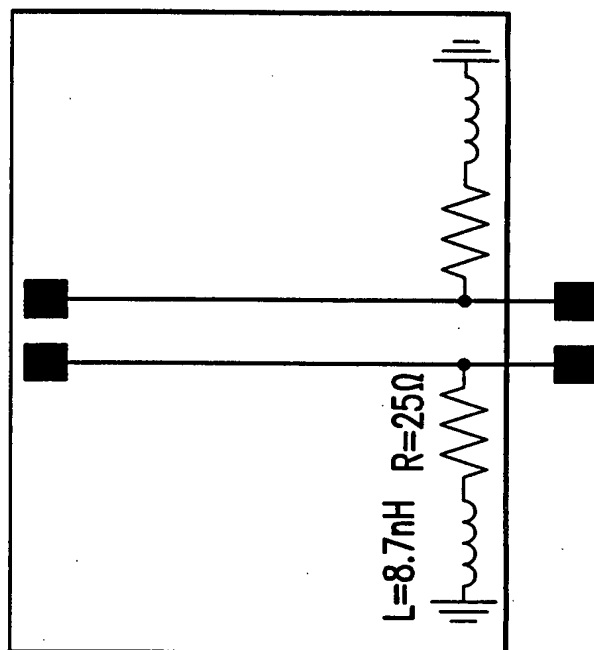


FIG. 3F

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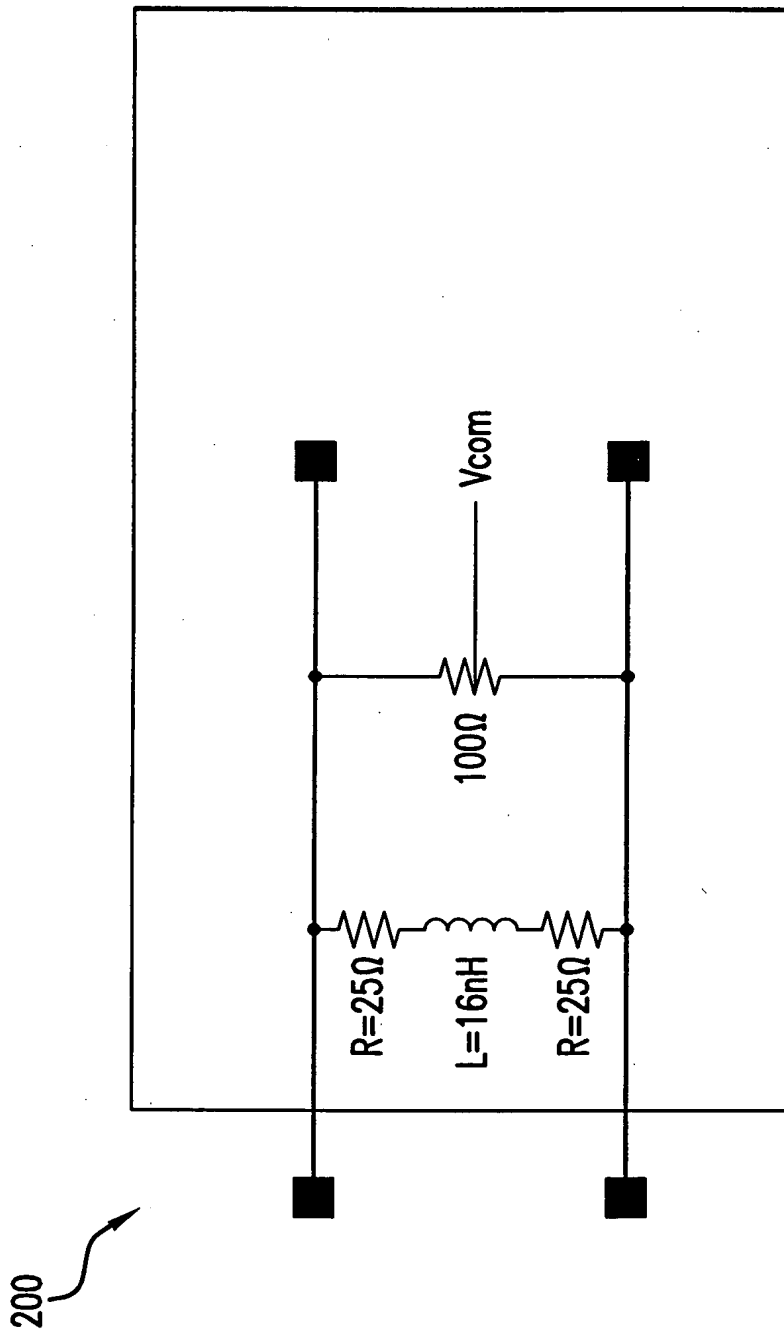


FIG. 4A

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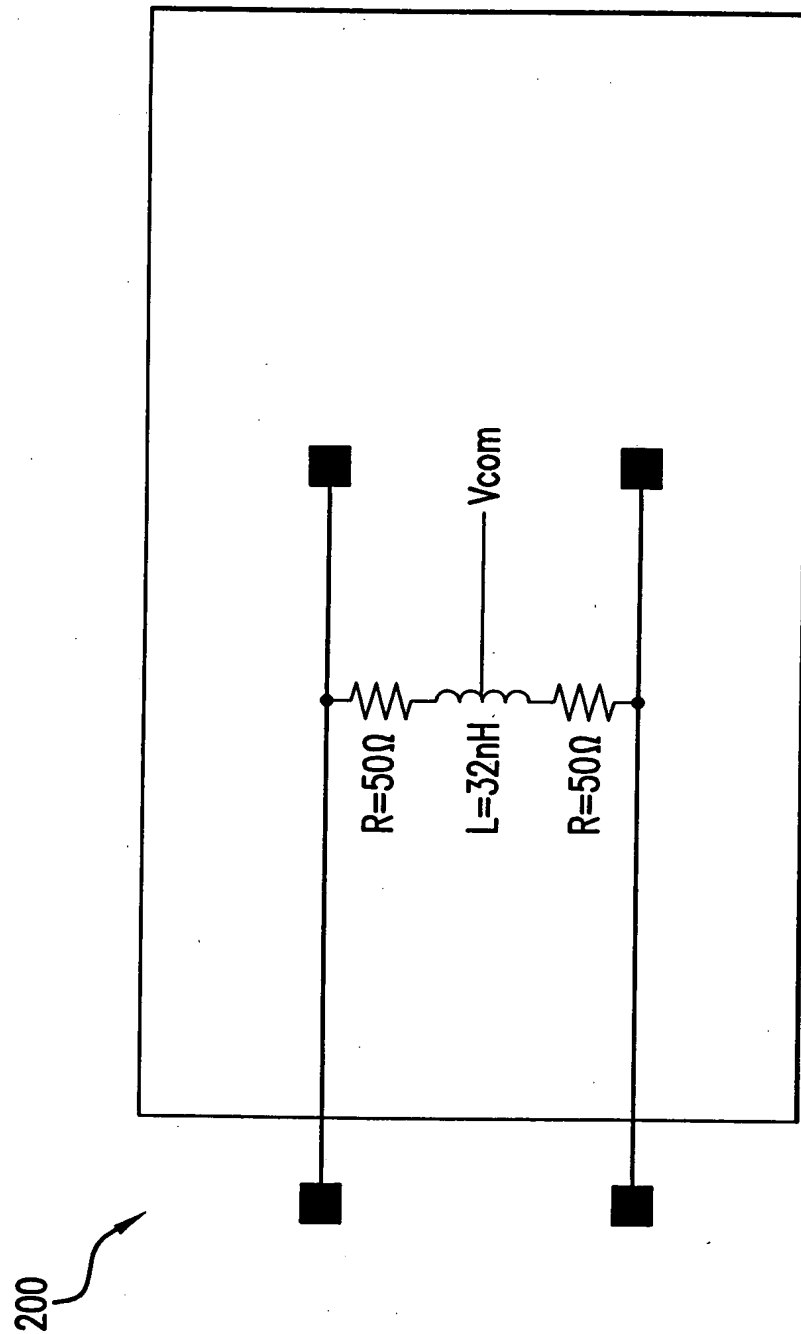


FIG. 4B

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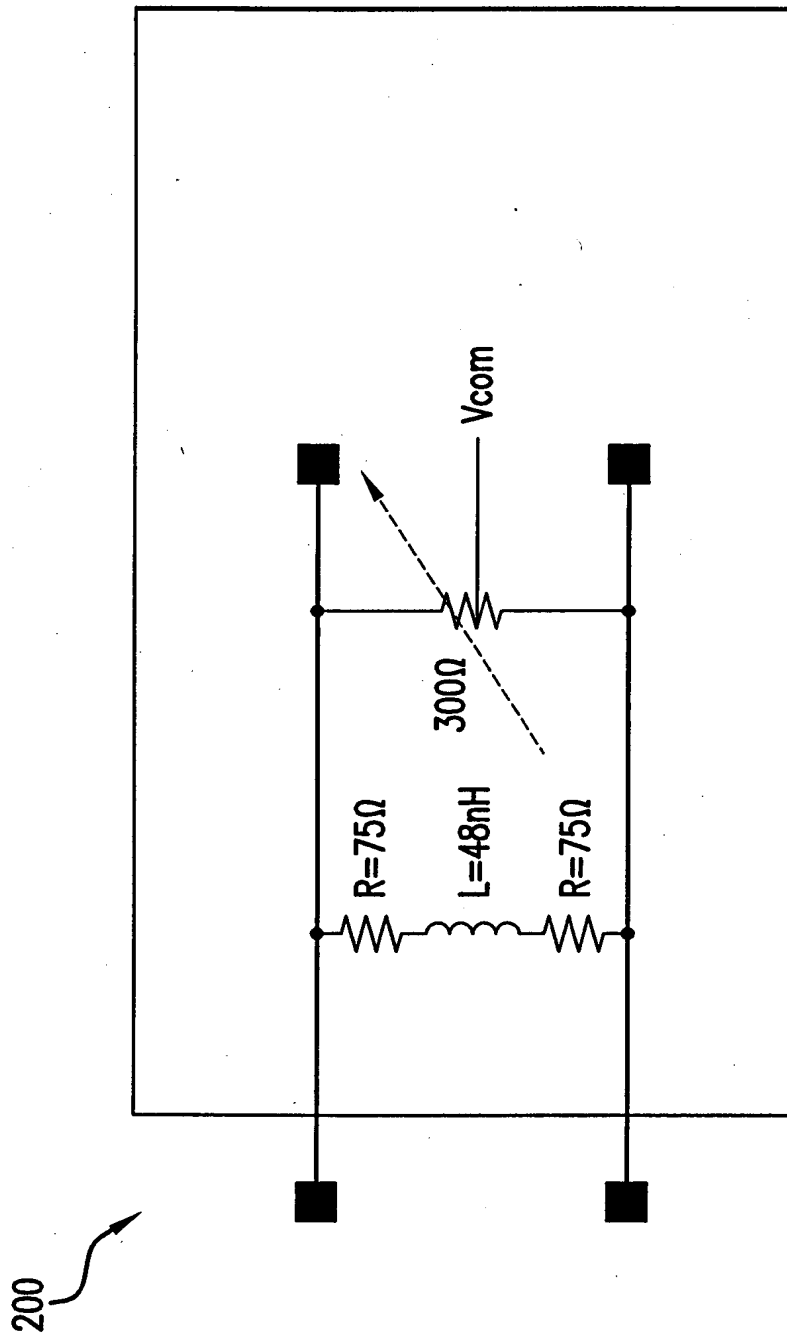
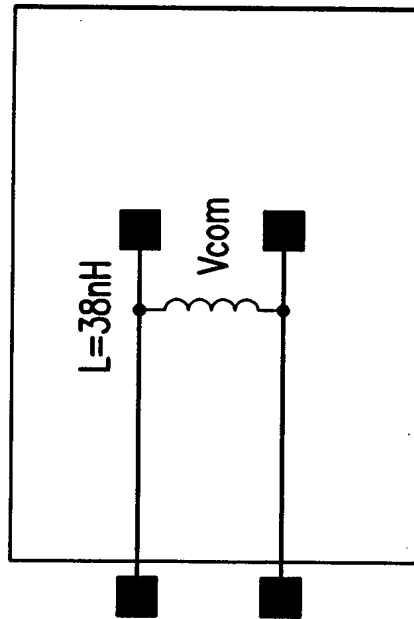
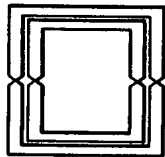


FIG. 4C

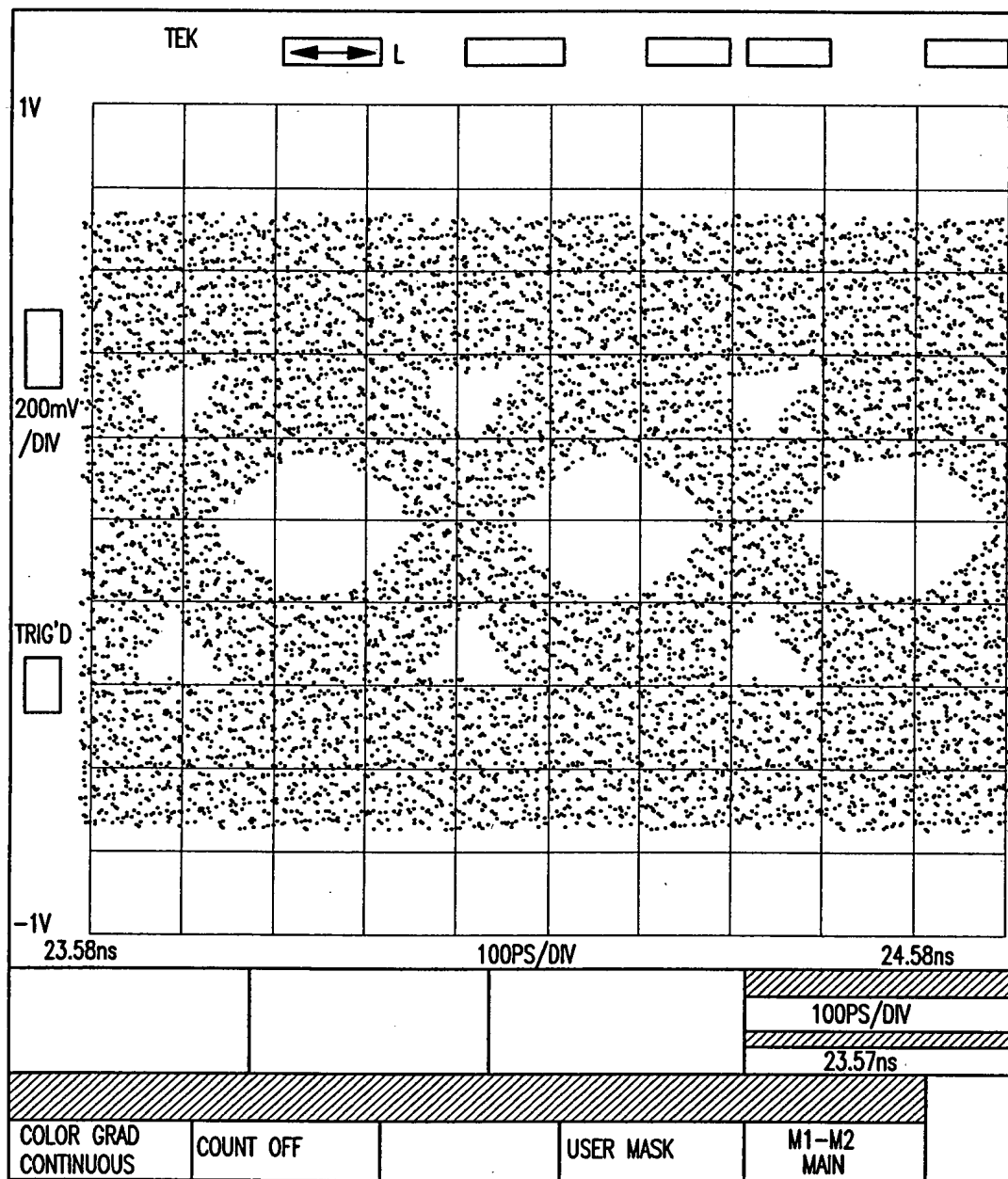
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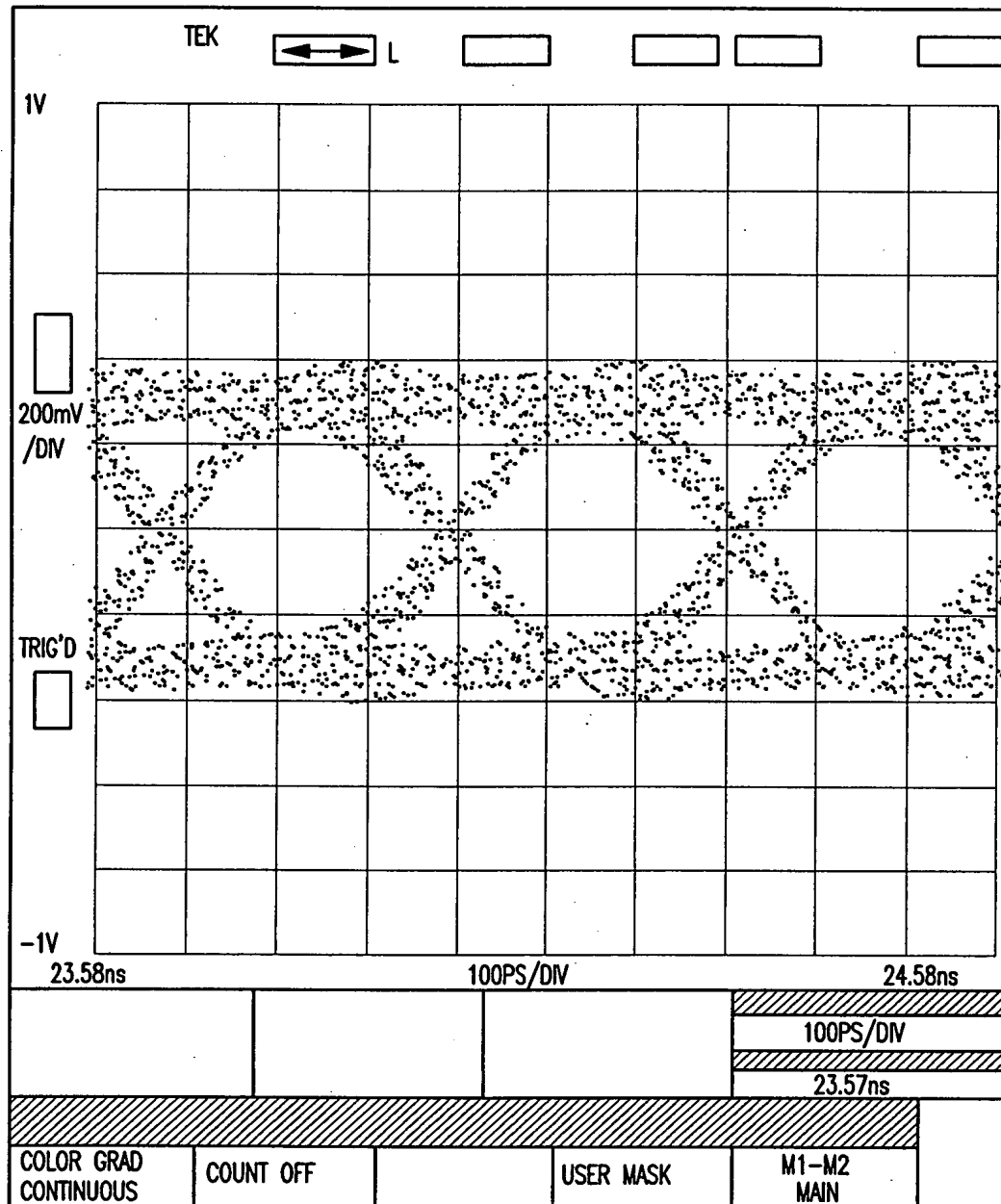
FIG. 5

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NO PRE-EMPHASIS
 FIG.6

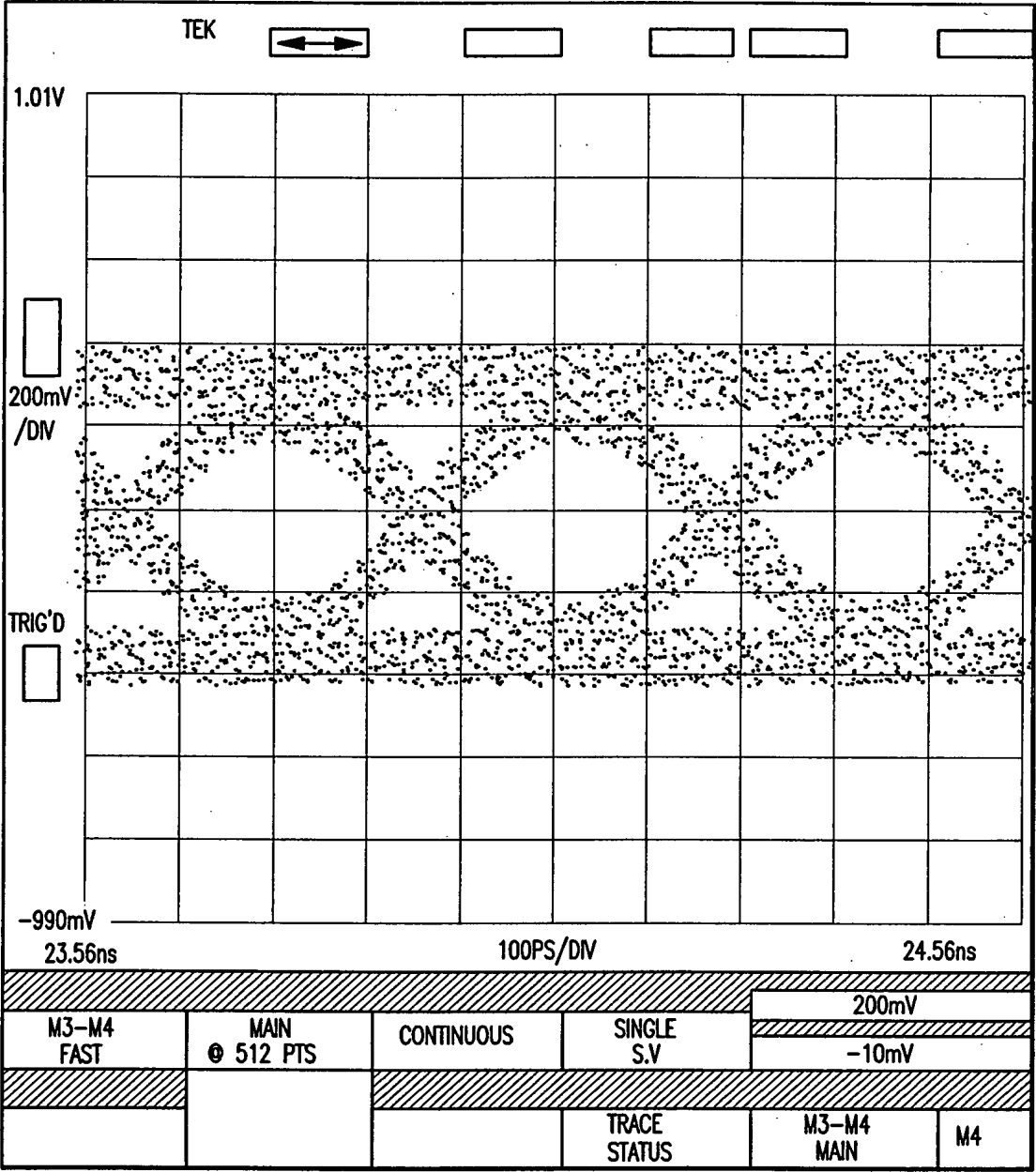
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PRE-EMPHASIS ON

FIG.6-1

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EXTERNAL EQUALIZER

FIG.6-2

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RANDOM EYE DIAGRAM @ 3.125 Gb/s "TYPICAL FR-4 BACKPLANE MODEL"

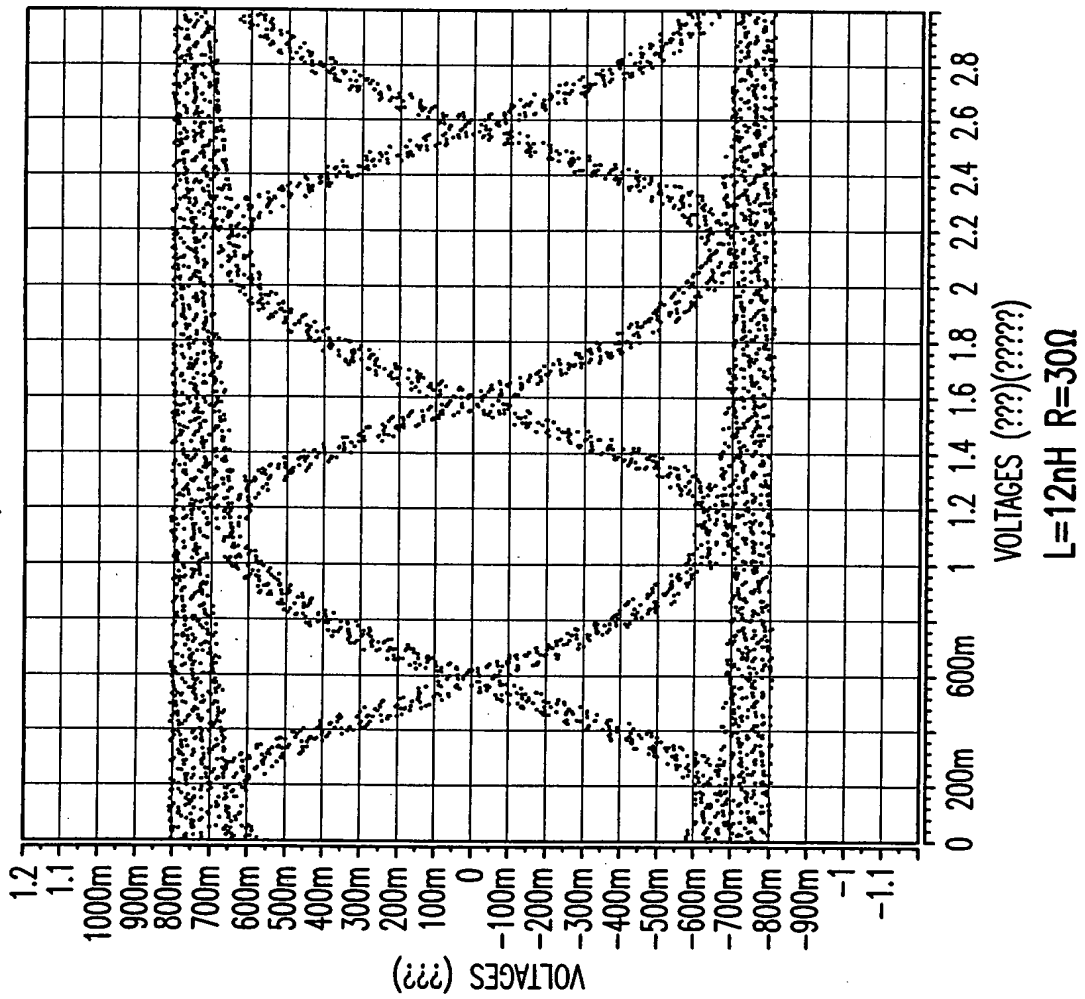
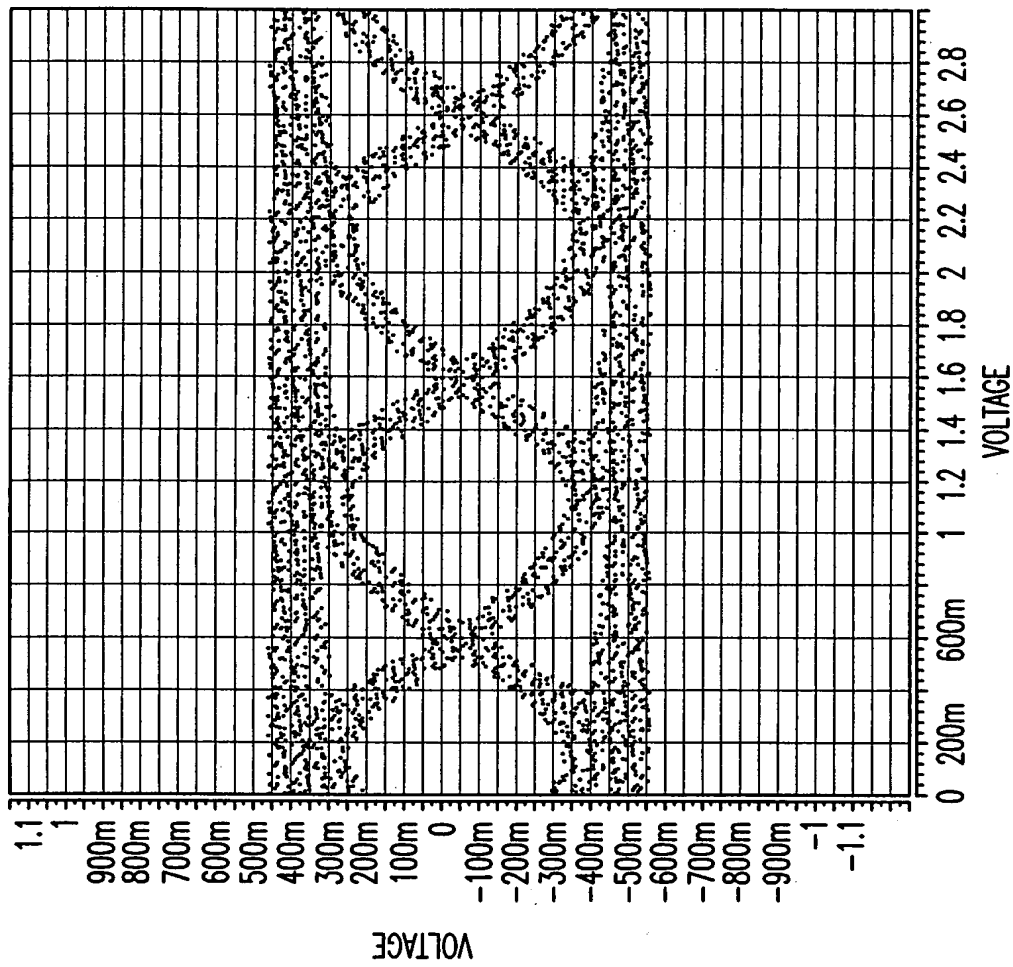


FIG. 7

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RANDOM EYE DIAGRAM @ 3.125 Gb/s "TYPICAL FR-4 BACKPLANE MODEL"



PRE-EMPHASIS

FIG. 7-1

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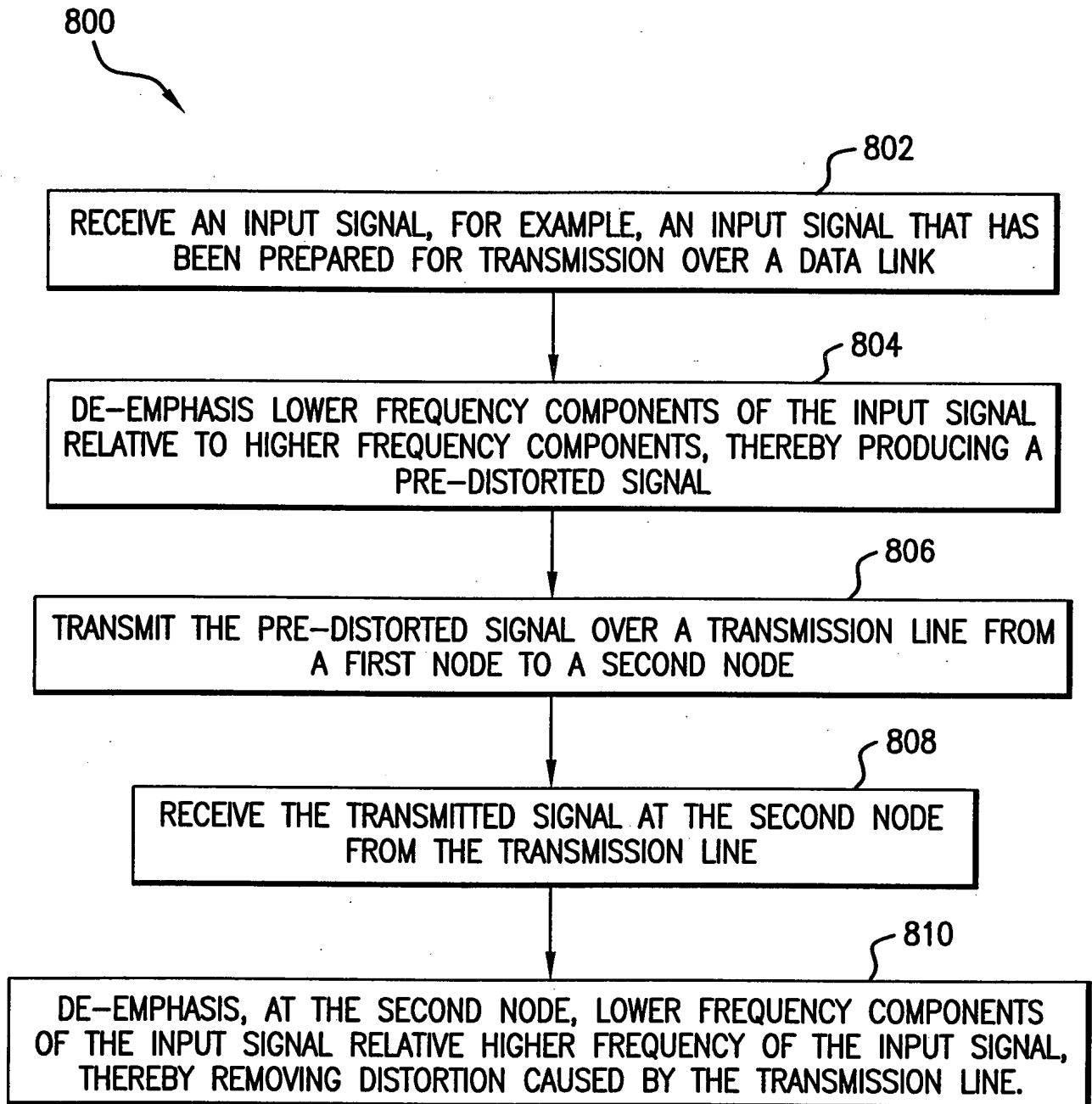


FIG.8

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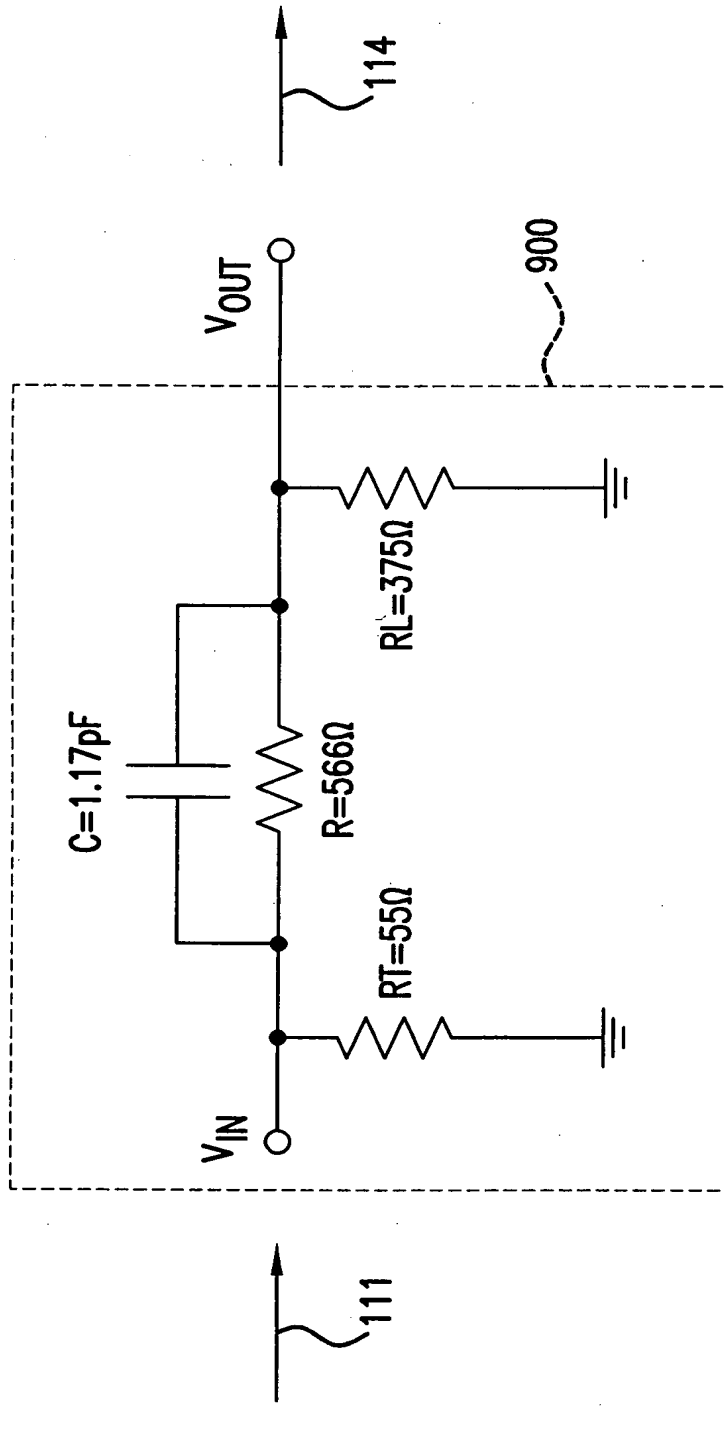


FIG. 9